

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings of claims in the application:

**Listing of Claims:**

1-9 Canceled

10. (Currently amended) A device for determining the frame boundaries of serialized framed data, the device comprising:

a serial to parallel converter for converting the serialized framed data to a parallel framed data;

a first register for receiving a second output of the parallel framed data from the serial to parallel converter;

a second register for receiving a first output of the parallel framed data from the first register;

a first selector for selecting a portion of the first output of the parallel framed data and a portion of the second output of the parallel framed data to form a data word wherein the data word is a concatenation of the portion of the first and second outputs of the parallel framed data;

a second selector for selecting a first one of the second output of parallel framed data and the concatenated data word;

a guesser for guessing a position of a frame boundary in the data selected by the second selector parallel framed data;

a rotator for rotating the selected parallel framed data in accordance with the position guessed by the guesser;

a register for storing a delayed version of the rotated parallel framed data;

a third selector for selecting a portion of the rotated parallel framed data held in the rotator and a portion of the delayed version of the rotated parallel framed data held in the third register to form a data output comprising multiple frames of the serialized framed data;

a tester for testing the data output to determine if the frame-boundary is boundaries are at [[a]] predetermined positions in the data output;

a counting mechanism for counting when[[ the]] a frame boundary is at[[ the]] a predetermined position in the data output and further counting when a frame boundary is not at a predetermined position; and

a state machine, the state machine determining if the device is in a state of synchronization based on the counting mechanism, said state machine further causing the second selector to select a second one of the second output of parallel framed data and the concatenated data word if the device is determined to not be in a state of synchronization.

11. (Currently amended) The device of claim 10 further comprising:  
an exhaust register, the exhaust register storing one or more positions guessed by the guesser determined not to contain a frame boundary at a position guessed by the guesser.

12. (Previously presented) The device of claim 11, wherein the guesser excludes the one or more positions stored in the exhaust register as possible positions of the frame boundary.

13. (Currently amended) The device of claim 10, further comprising:  
a shifter for shifting the parallel framed data by wherein the portion of the first output of the parallel framed data comprises bits of the first parallel framed data starting at an offset of an odd number of bits from the first arriving serial bits, and the portion of the second output of the parallel framed data comprises the first arriving odd number of bits from the second parallel framed data an odd number of bits; and  
a selector for selecting the parallel framed data or the parallel framed data shifted by the odd number of bits, wherein the selected data is provided to the rotator.

14. (Previously presented) The device of claim 13, wherein the odd number of bits is one.

15. (Previously presented) The device of claim 10, wherein the serialized framed data comprises a plurality of frames, each frame comprising a data field and a synchronization pattern.

16. (Previously presented) The device of claim 15, wherein the data field comprises 64 bits and the synchronization pattern comprises two bits.

17. (Previously presented) The device of claim 10, wherein the serialized framed data is 10 Gb Ethernet data.

18-20 Canceled